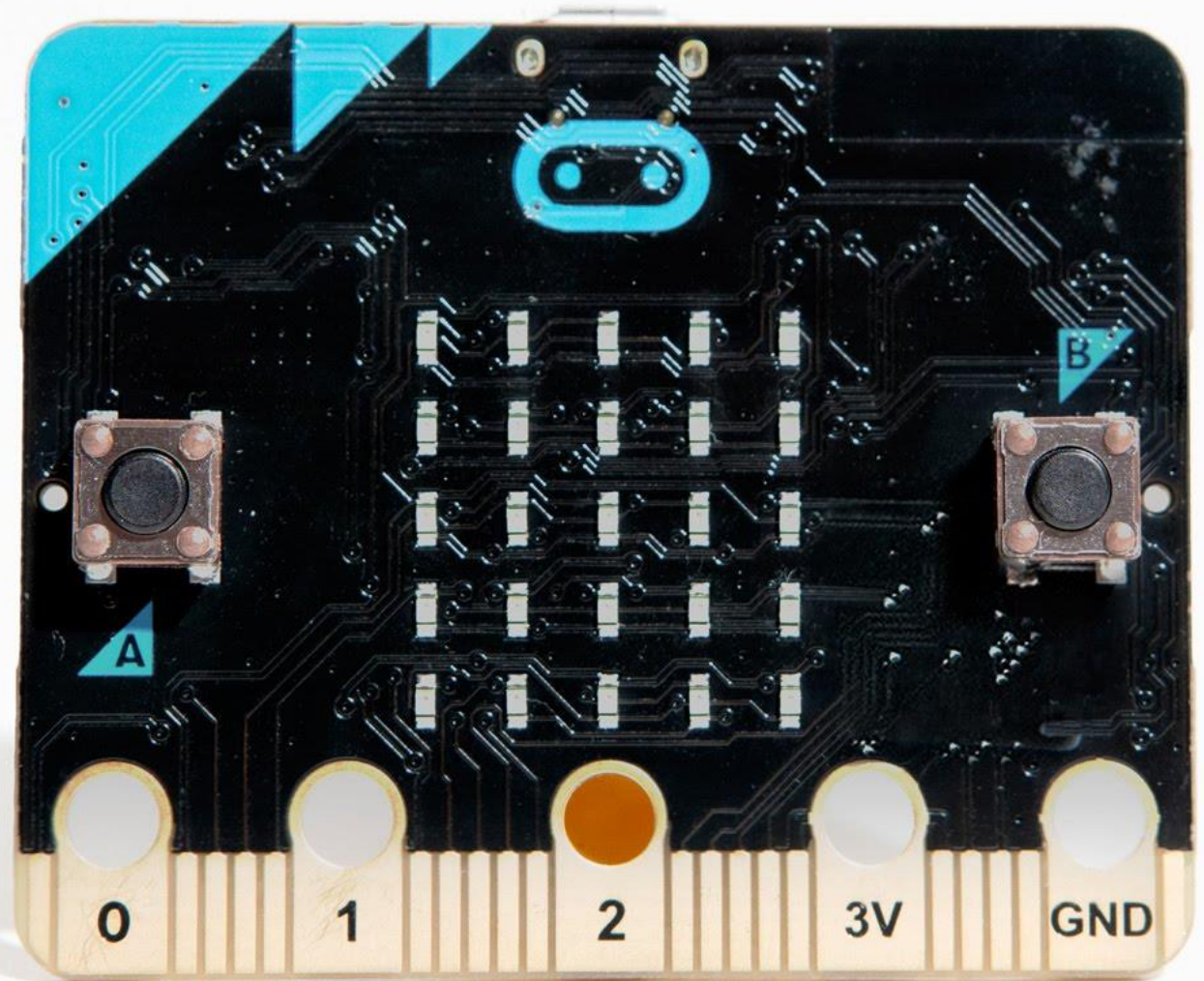
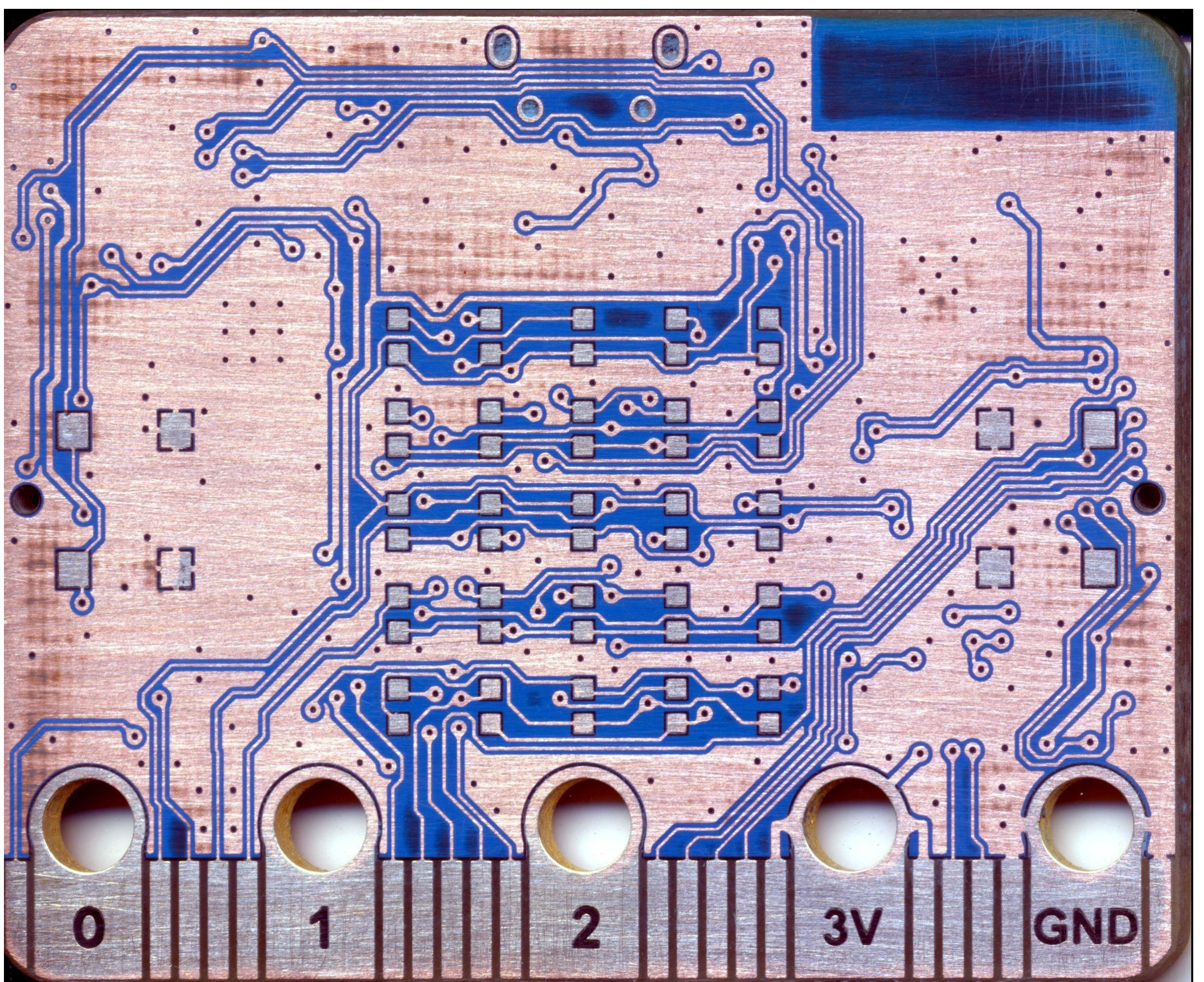


Reengineering
Micro:bit → schematics
V0.95



Top Layer

- Bauteile abgelötet
- Lötstopplack abgeschliffen
- Durchkontaktierungen, Via's und Leiterbahnen nachgezeichnet
- Zeichnung gespiegelt



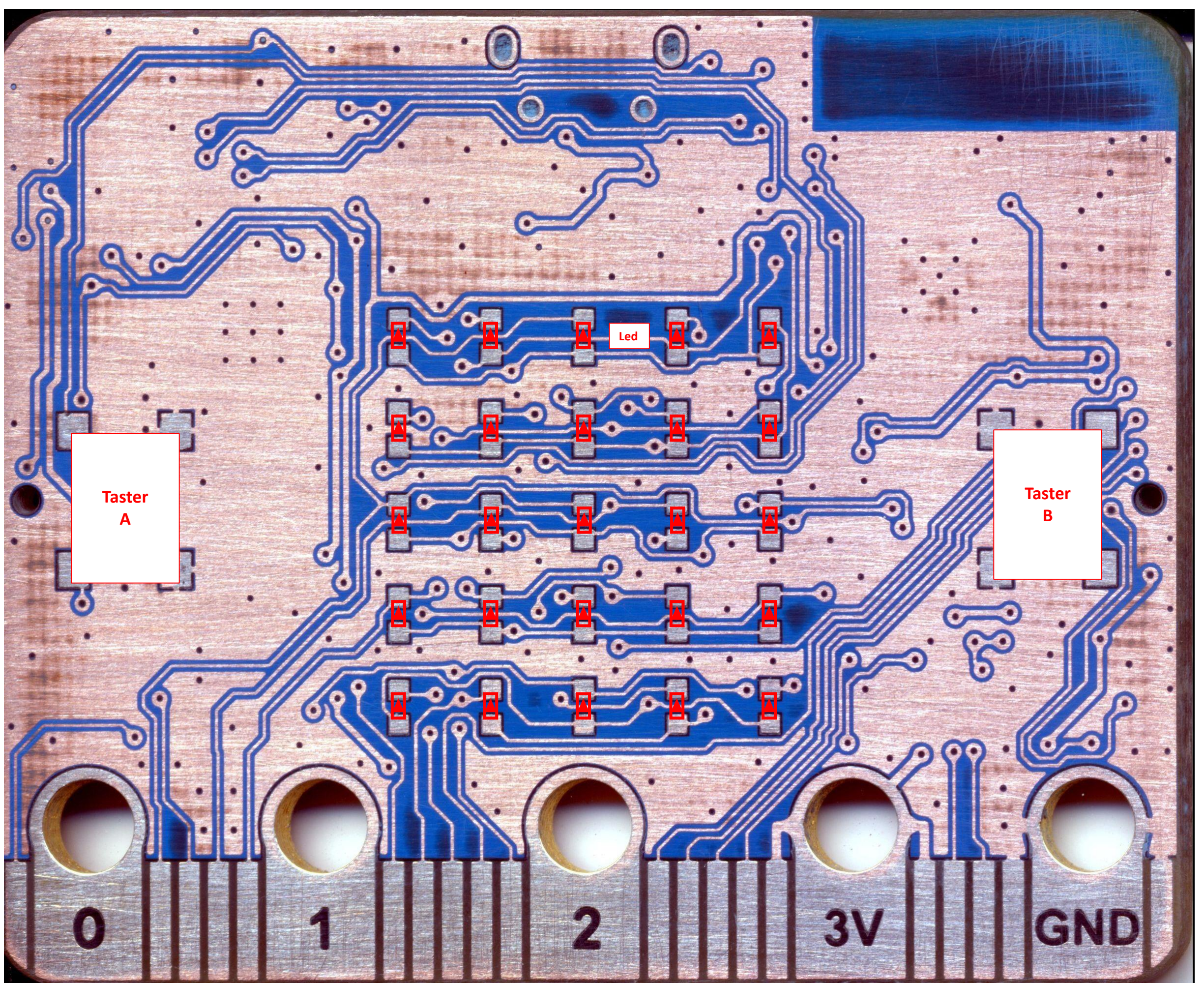
0

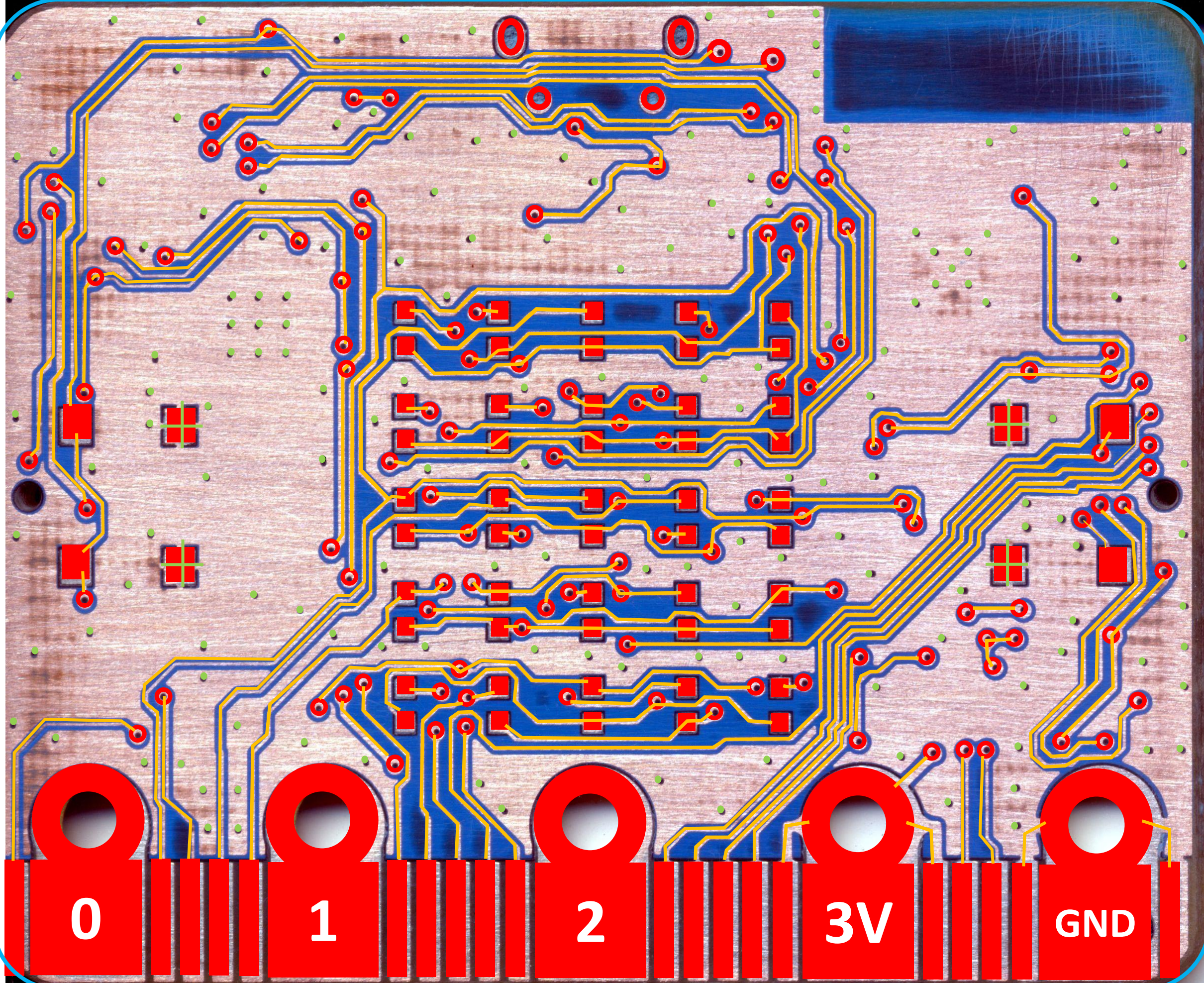
1

2

3V

GND





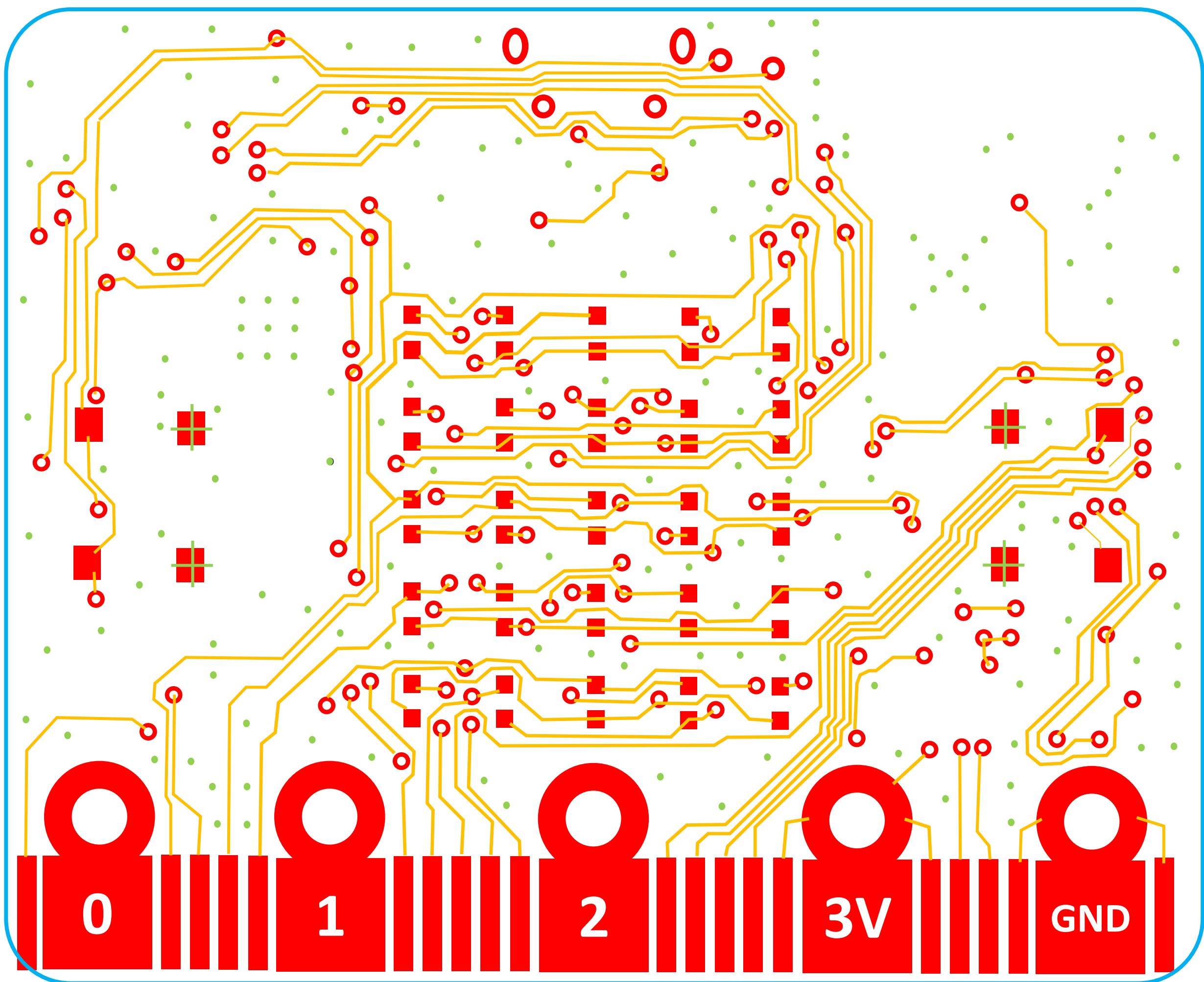
0

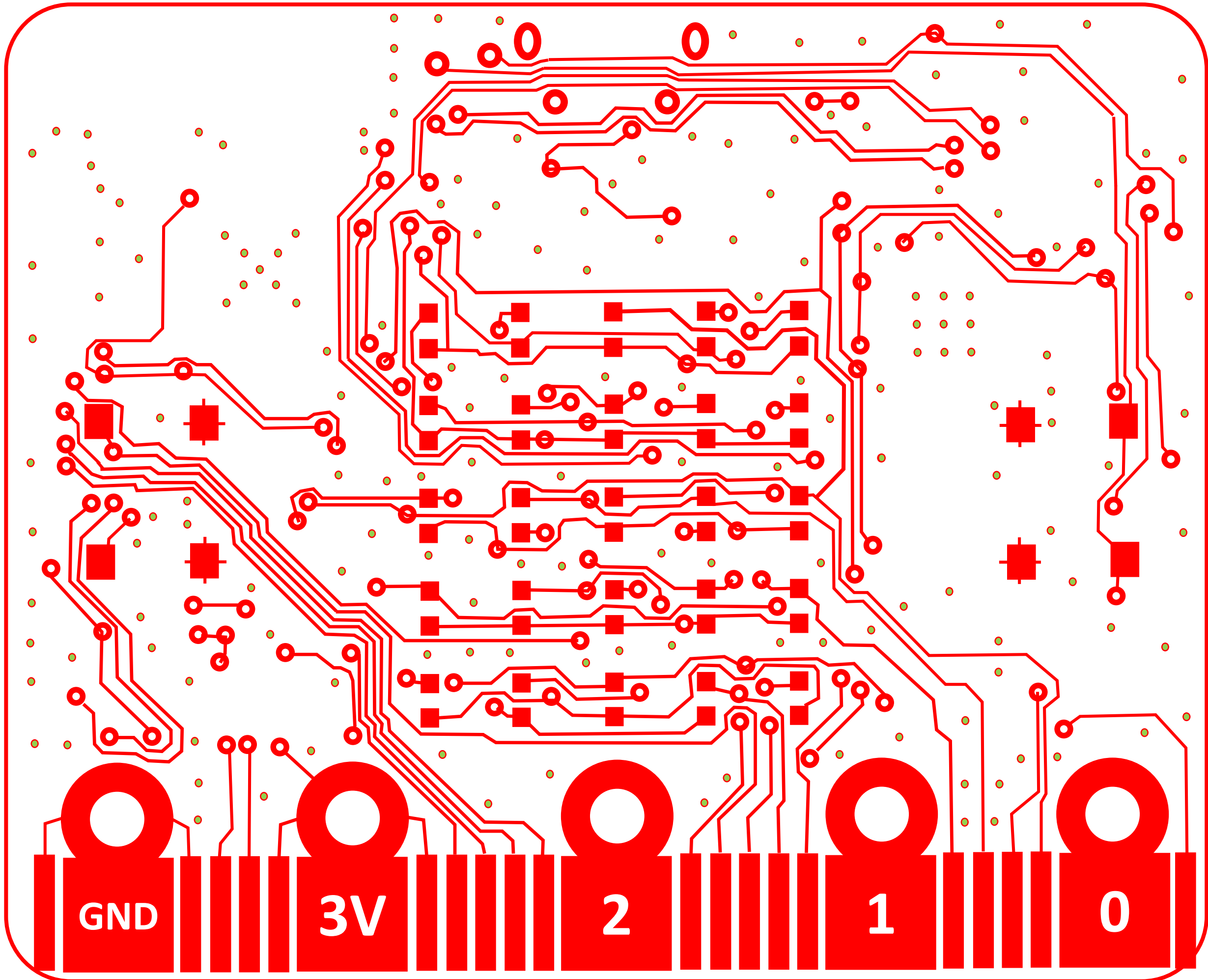
1

2

3V

GND





GND

3V

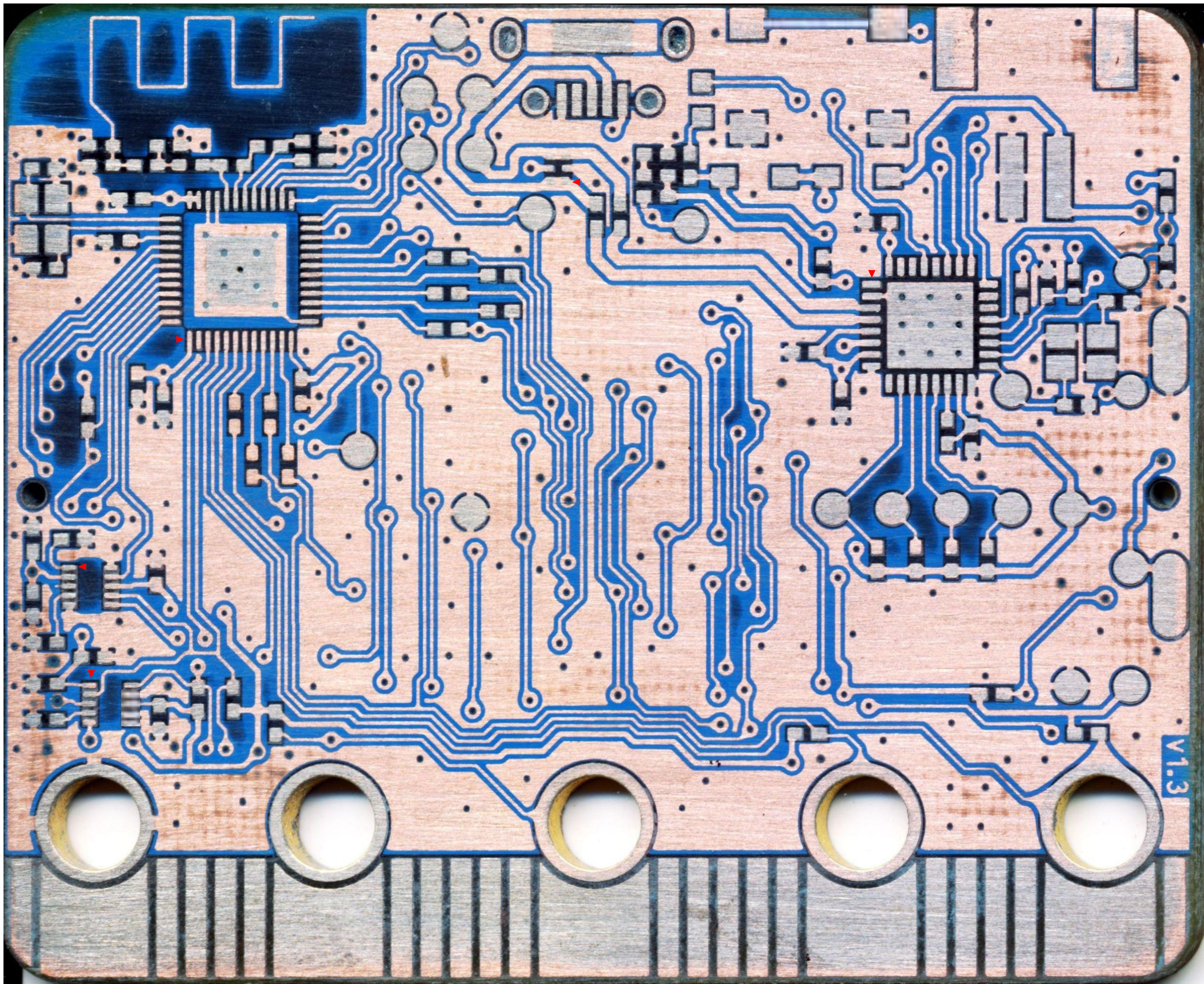
2

1

0

Bottom Layer

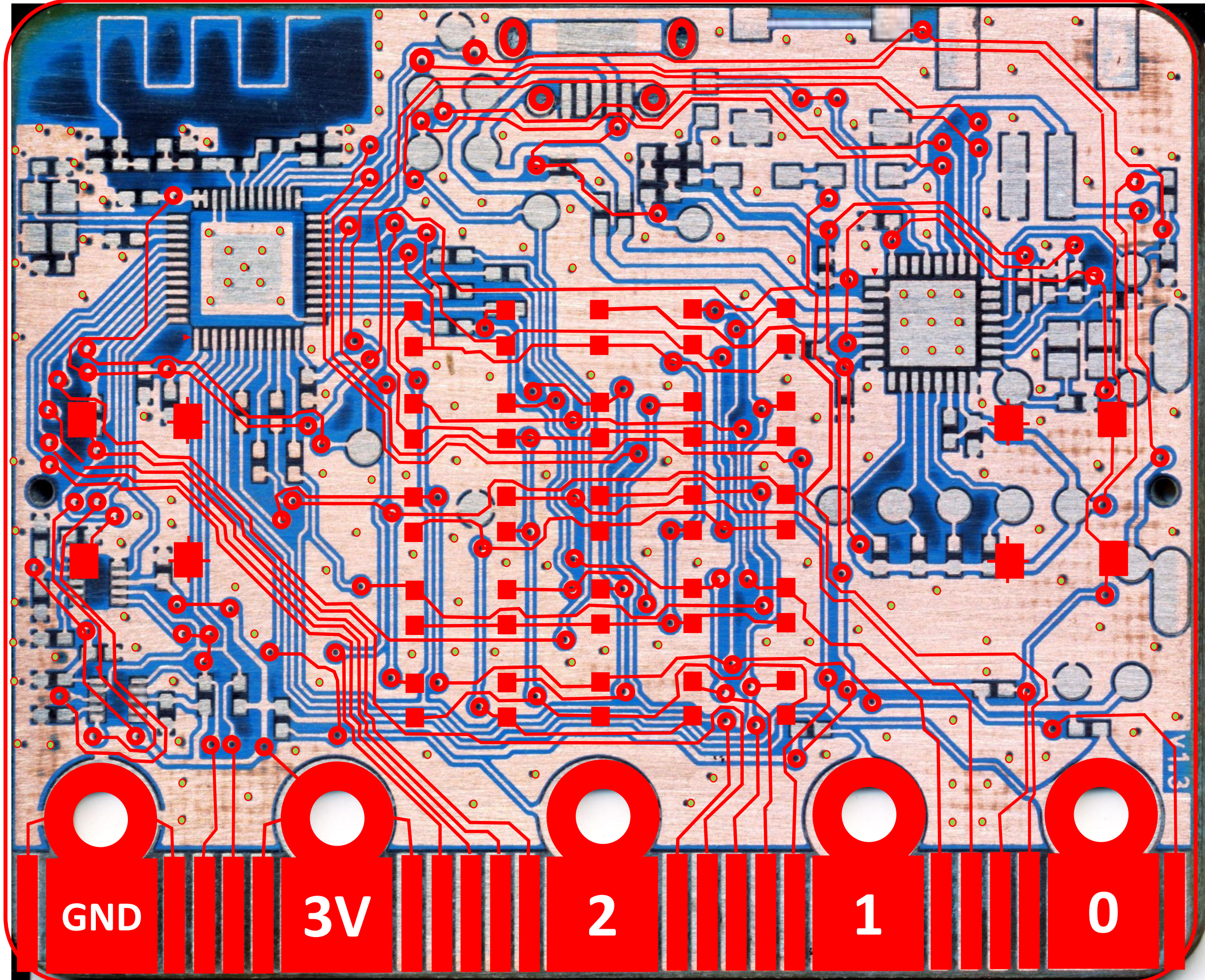
- Bauteile bestimmt und abgelötet
- **!!! Vorsicht bei den Werten der Bauteile !!!**
 - Kleine Bauform, lange Messleitungen zum LCR Meter
 - Kondensatoren, Spulen
 - Werte = ???
- Lötstopplack abgeschliffen
- **!!! Veränderungen am Bild !!!**
 - 2 Pads vom IC unten links fehlen
 - 1 Pad vom Taster rechts oben und die Leiterbahn zwischen den Pads
 - Fehler wurden grafisch behoben
 - Einfügen kleiner roten Pfeile für 1 Markierung



V1.3

Top & Bottom Layer

- Top Zeichnung und Bottom Bild übereinander gelegt
- Kleinere Ungenauigkeiten durch Skalierung



GND

3V

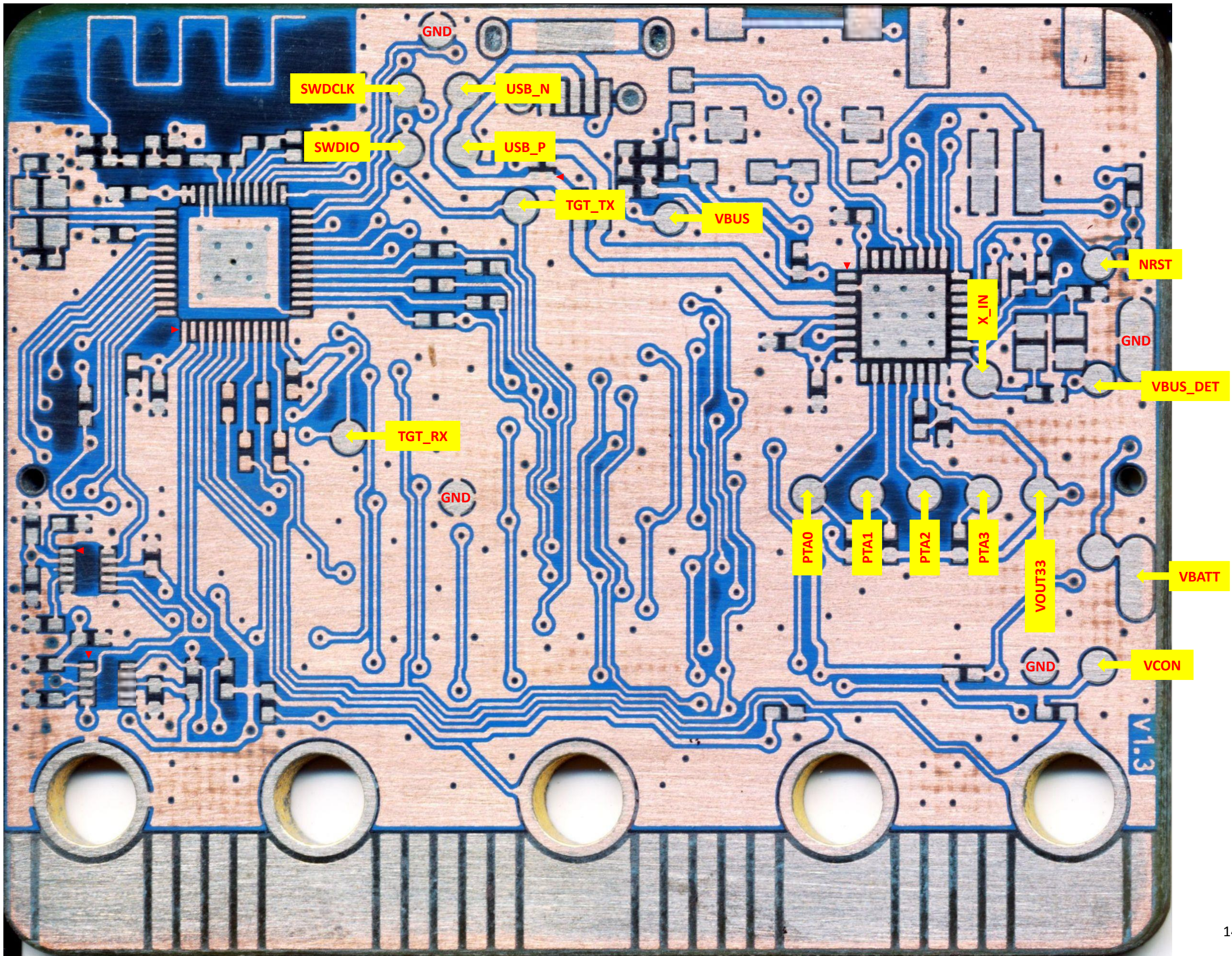
2

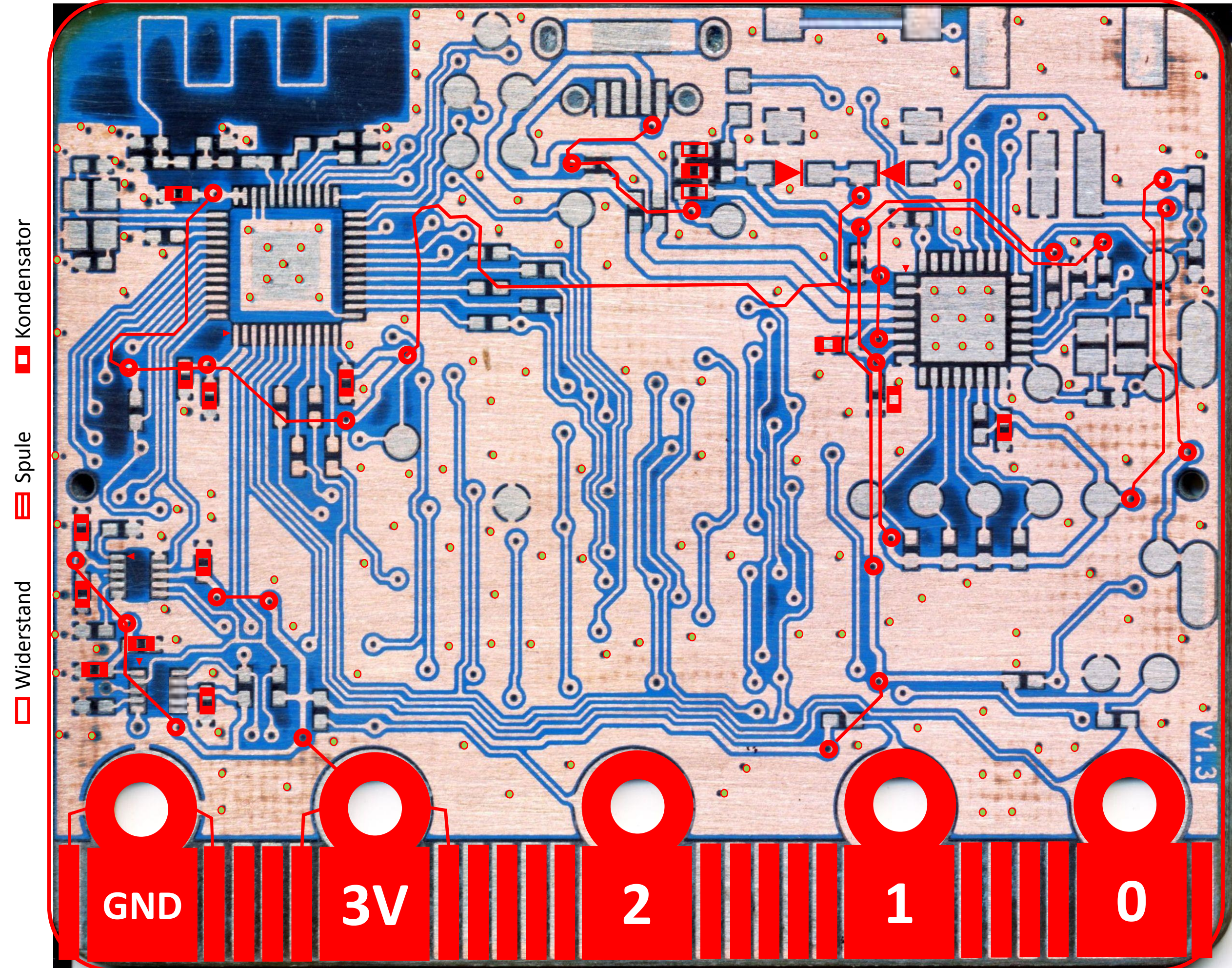
1

0

Testpunkte

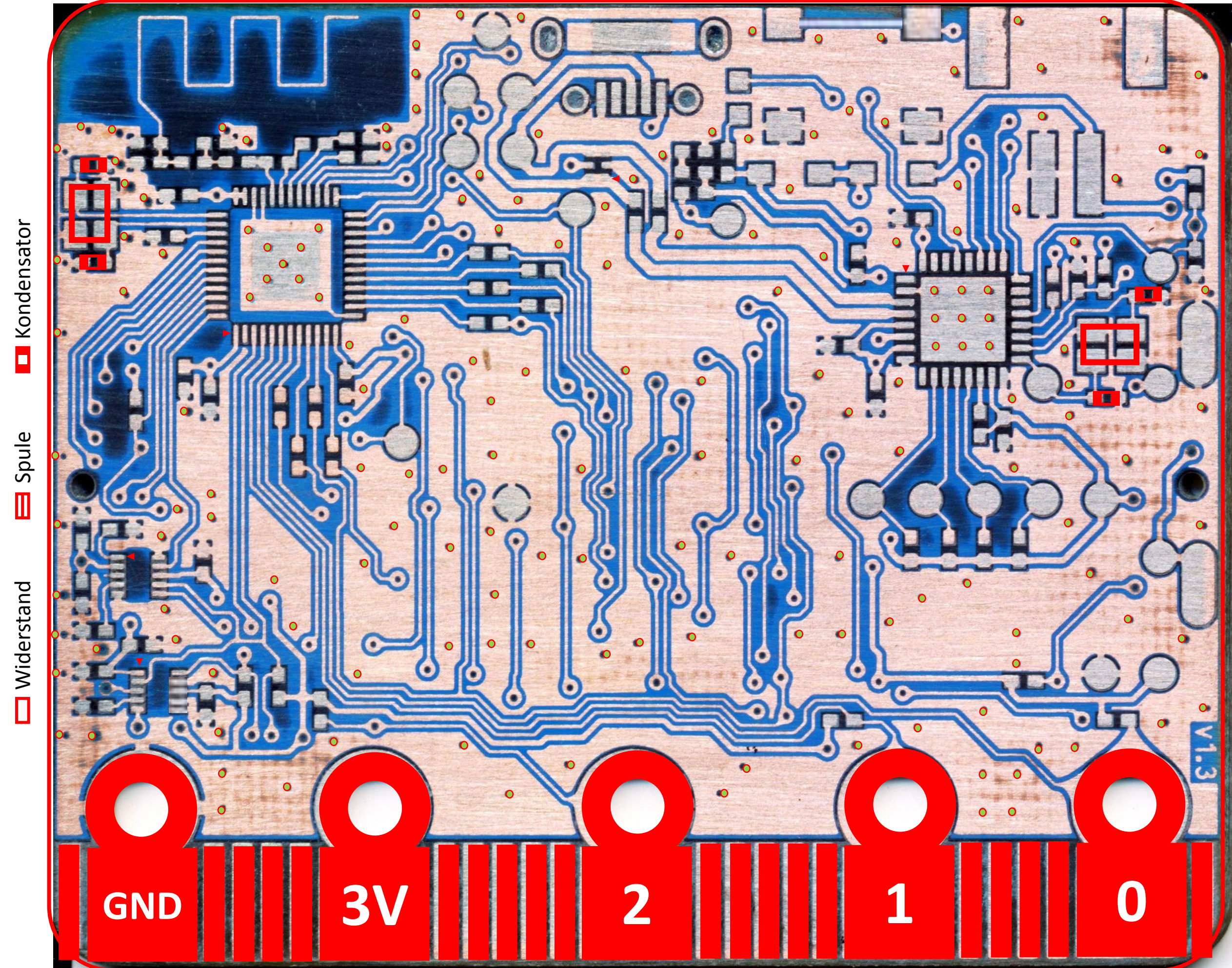
- Größere runde Goldflächen sind Testpunkte
- Programmierinterface
- Spannungskontrolle
- Frequenzkontrolle





Frequenzerzeugung

- 2x 16MHz Quarz
- Standard Quarzbeschaltung mit je 1 Kondensator am Ein- und Ausgang
- Werte $\sim 12\text{pF}$, hängt vom Quarz und vom IC ab



Widerstand

Spule

Kondensator

GND

3V

2

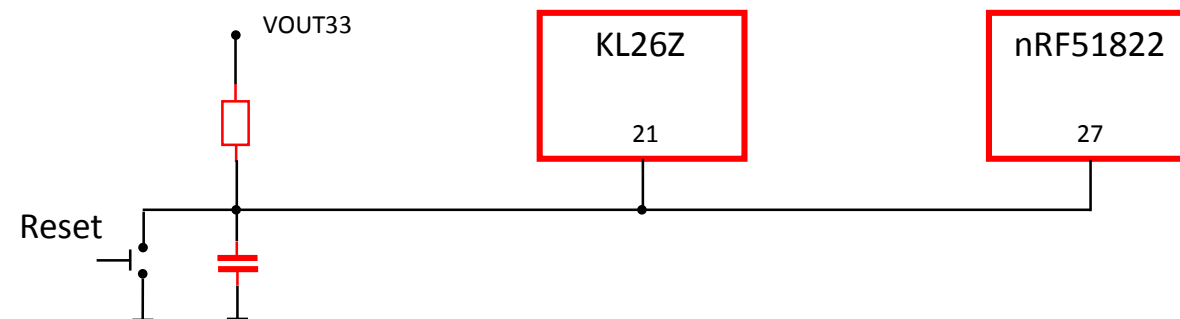
1

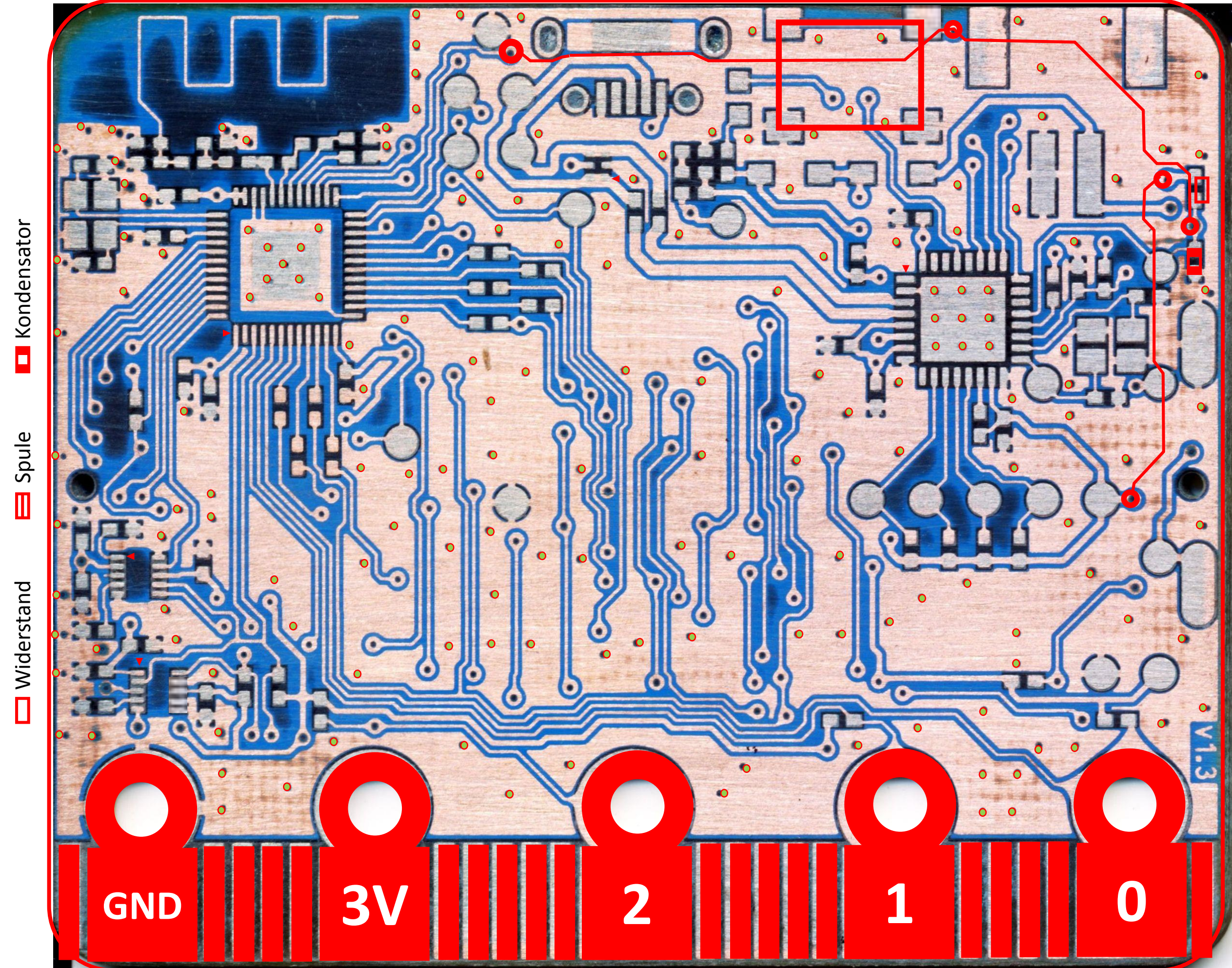
0

V1.3

Reset

- Taster auf dem Board





Widerstand

Spule

Kondensator

GND

3V

2

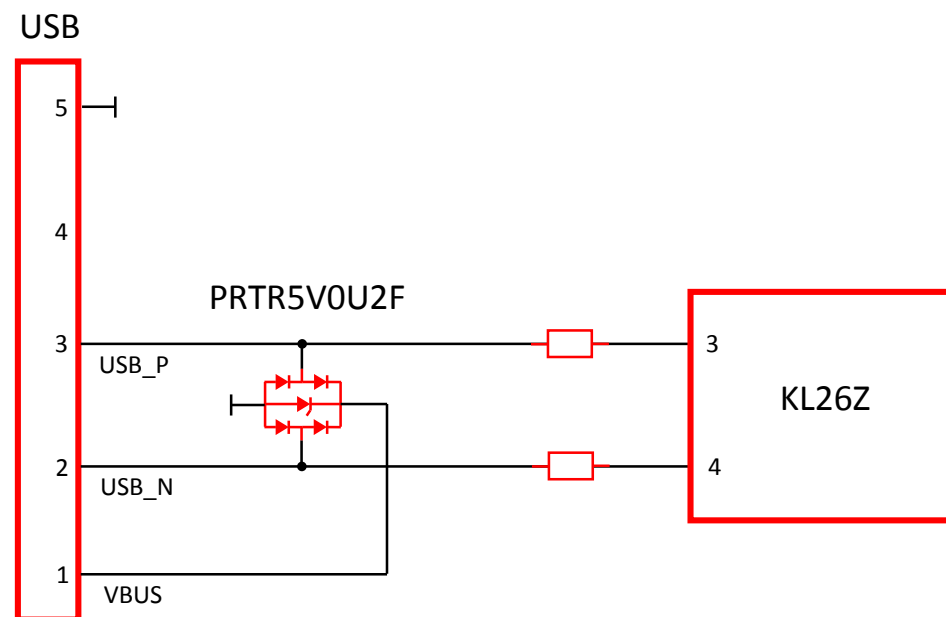
1

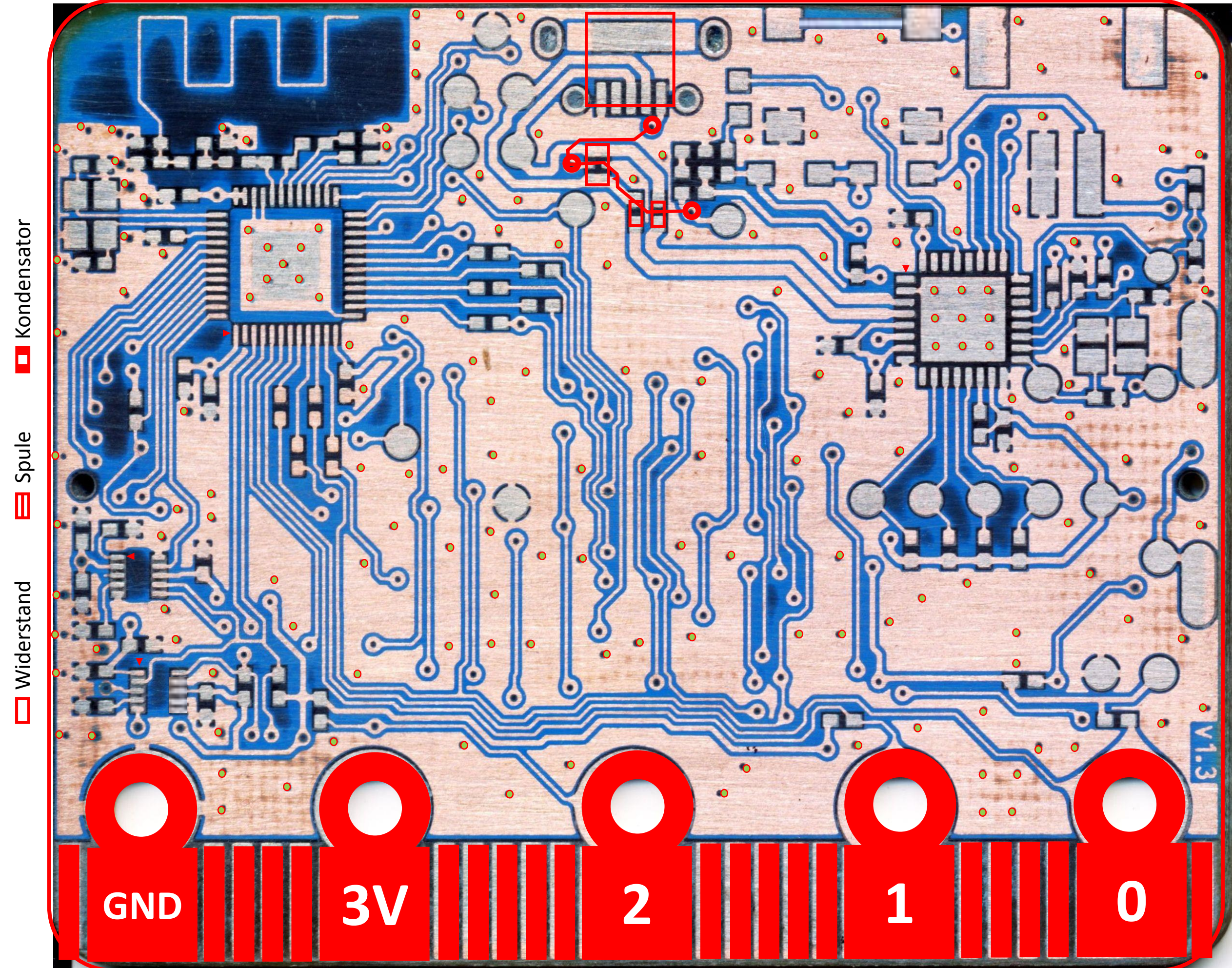
0

V1.3

USB

- Connector USB Micro





Widerstand

Spule

Kondensator

GND

3V

2

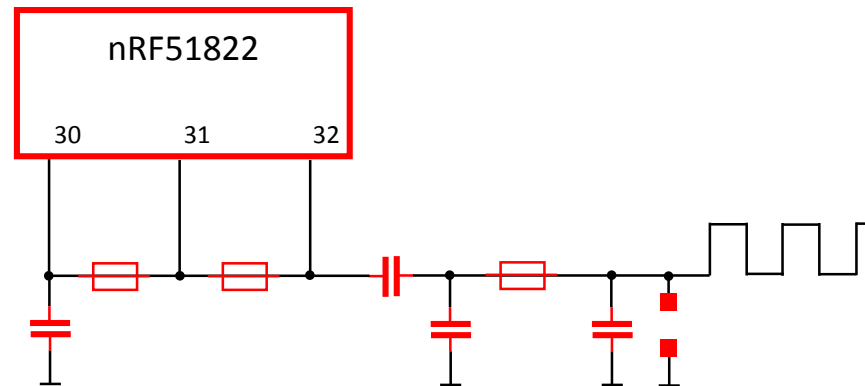
1

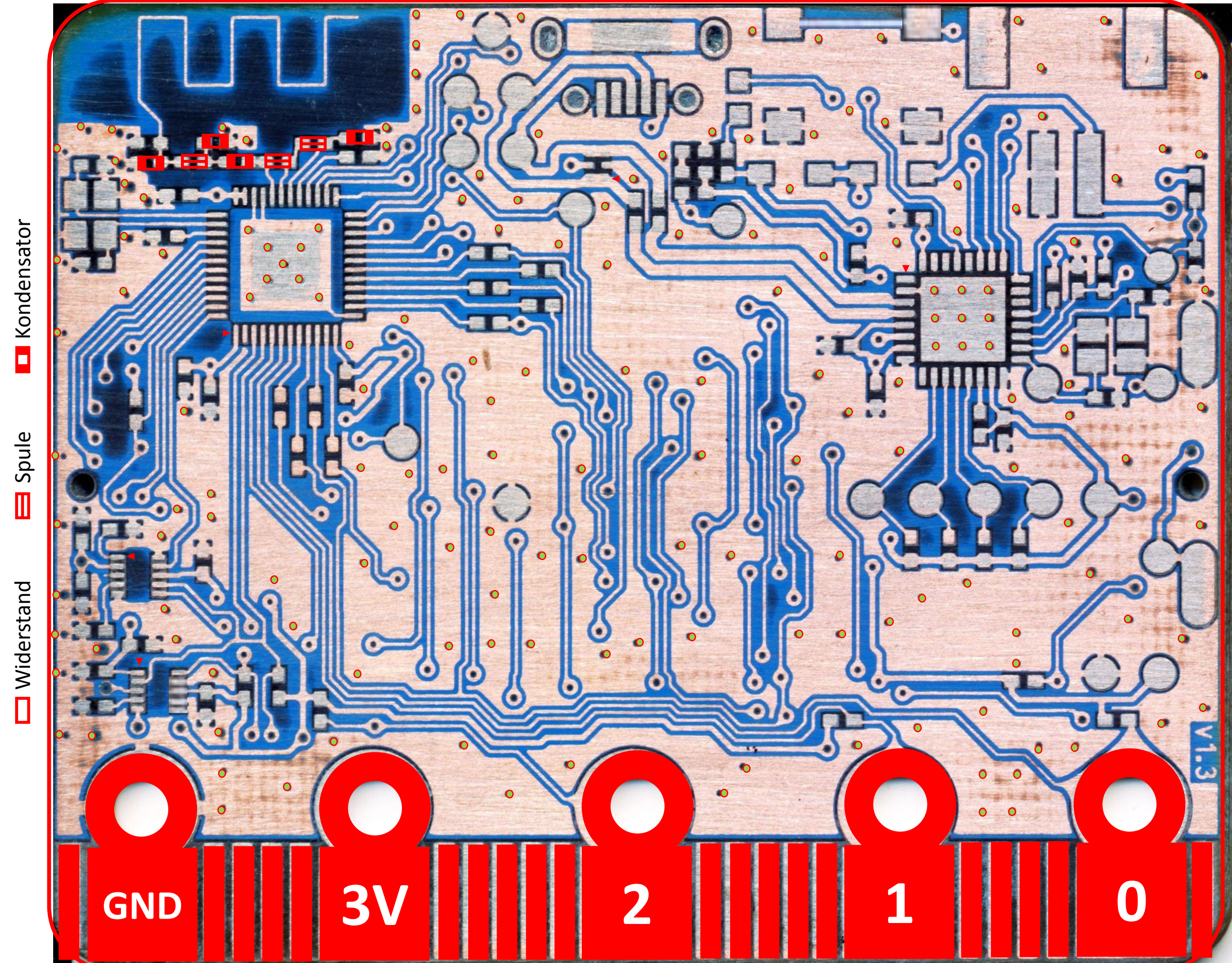
0

V1.3

Bluetooth

- 2,4 GHz





Widerstand Spule Kondensator

GND

3V

2

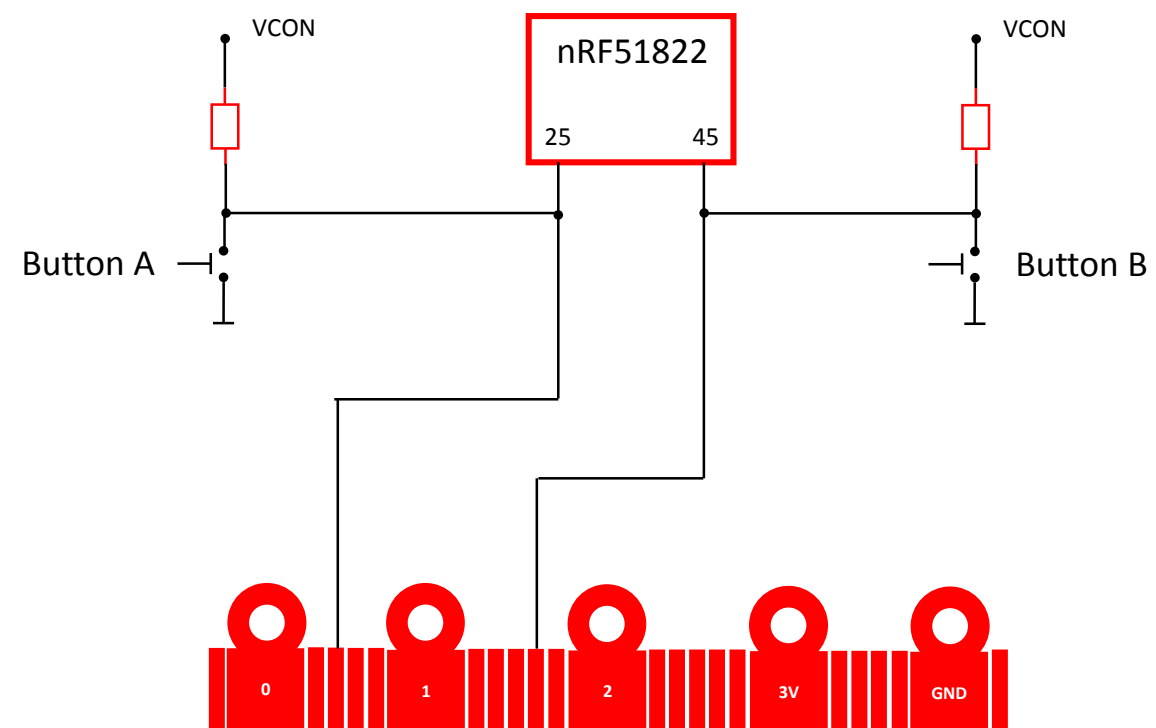
1

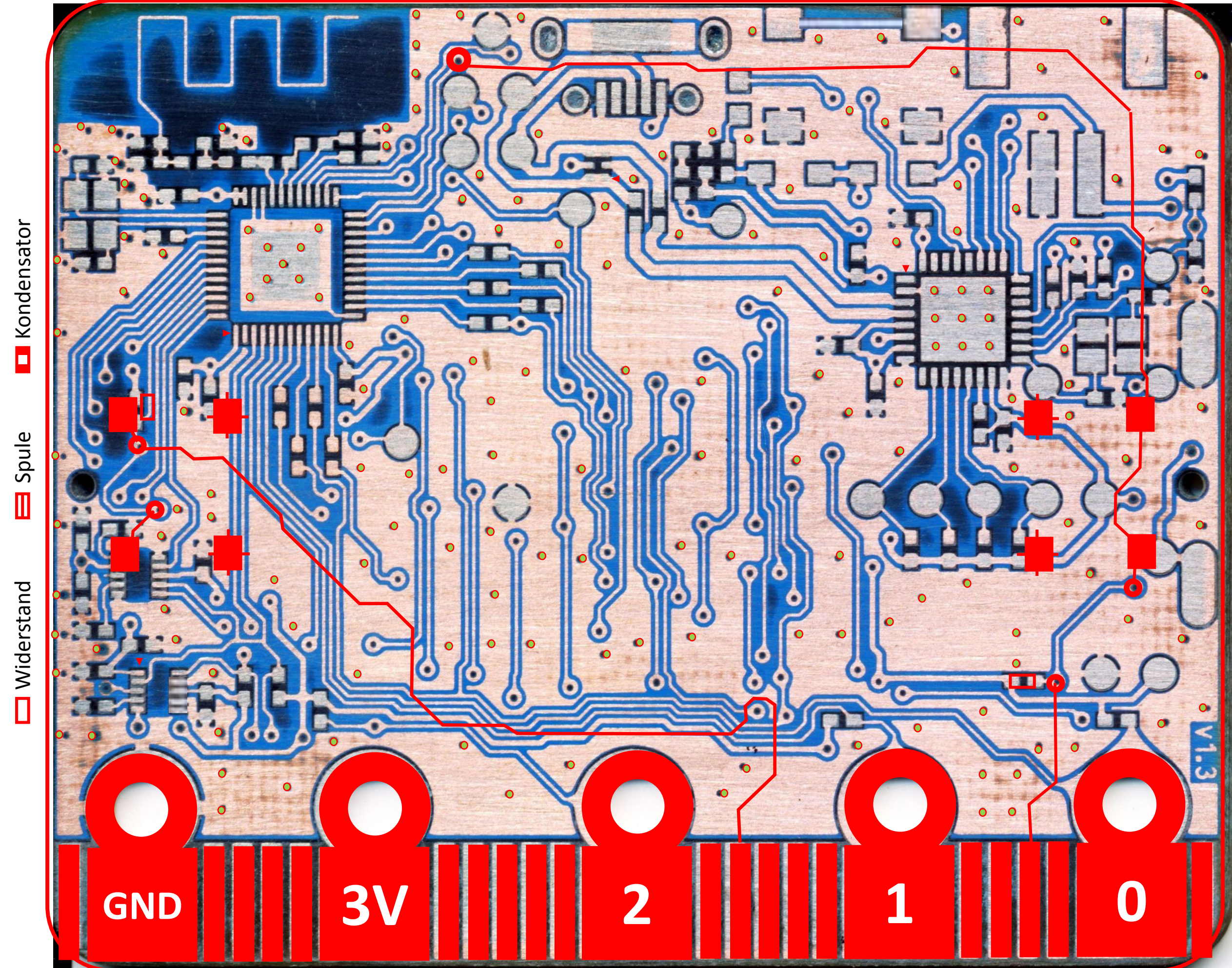
0

V1.3

Tasten

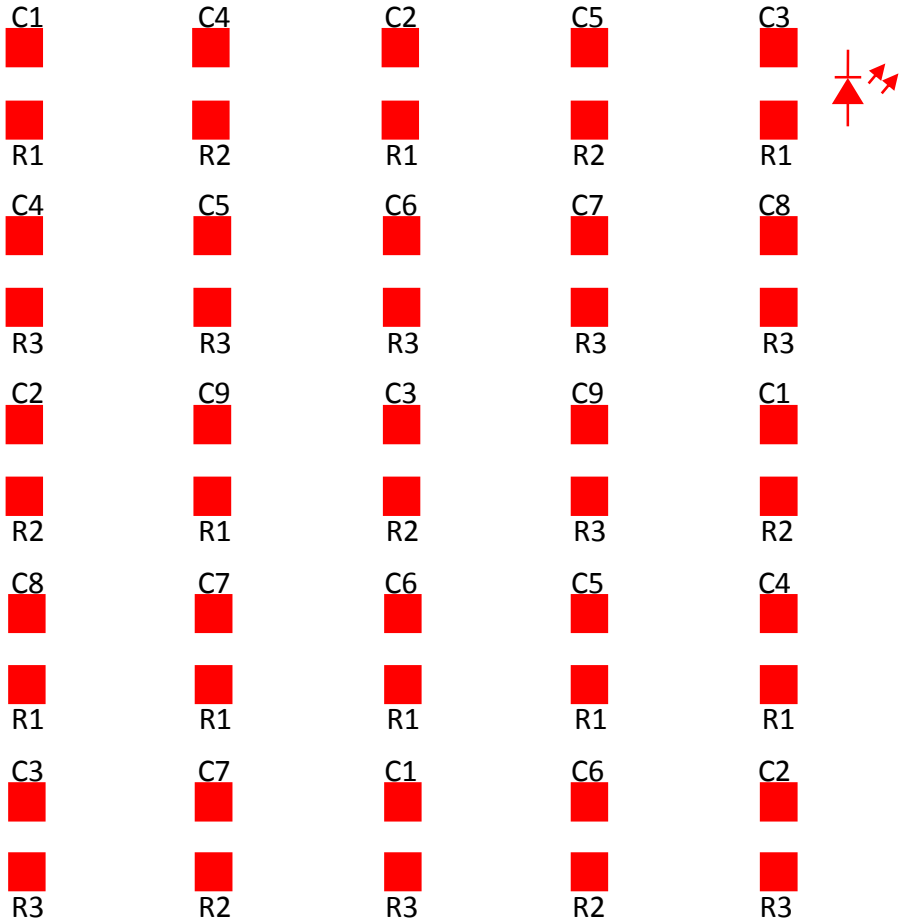
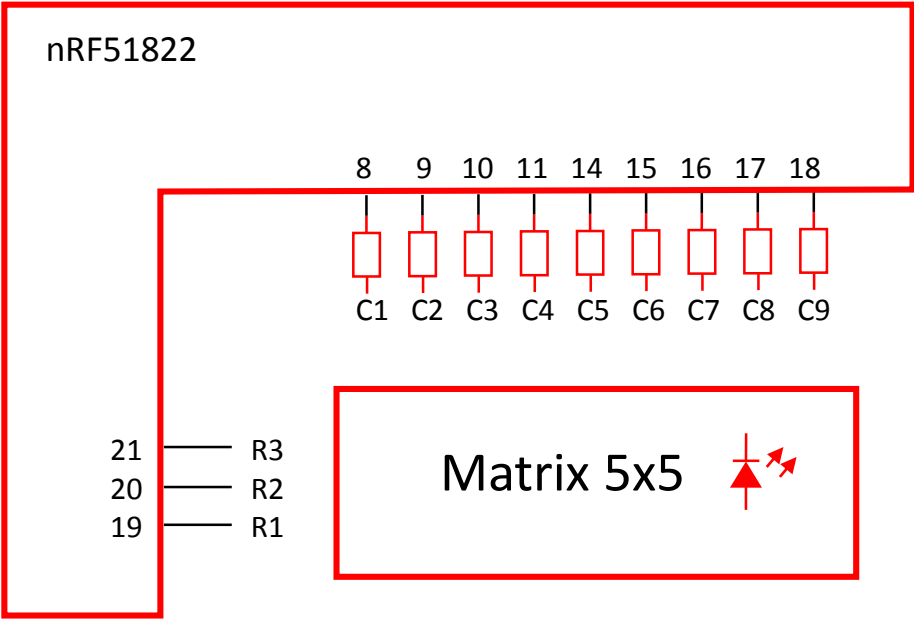
- 2 Tasten, A und B



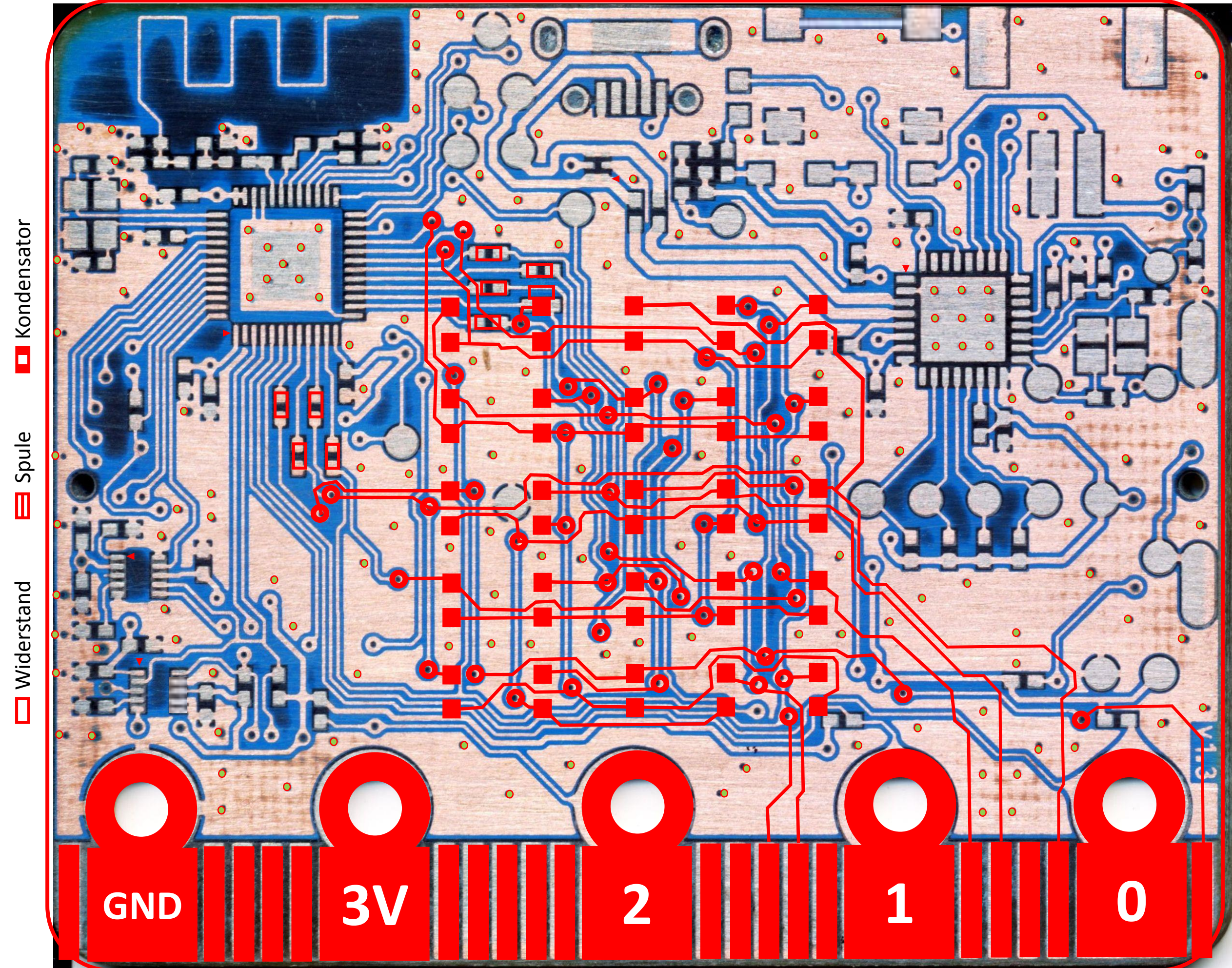


Anzeige

- Led Matrix 5x5
- Column 1..9
- Row 1..3



Front View



Widerstand
Spule
Kondensator

GND

3V

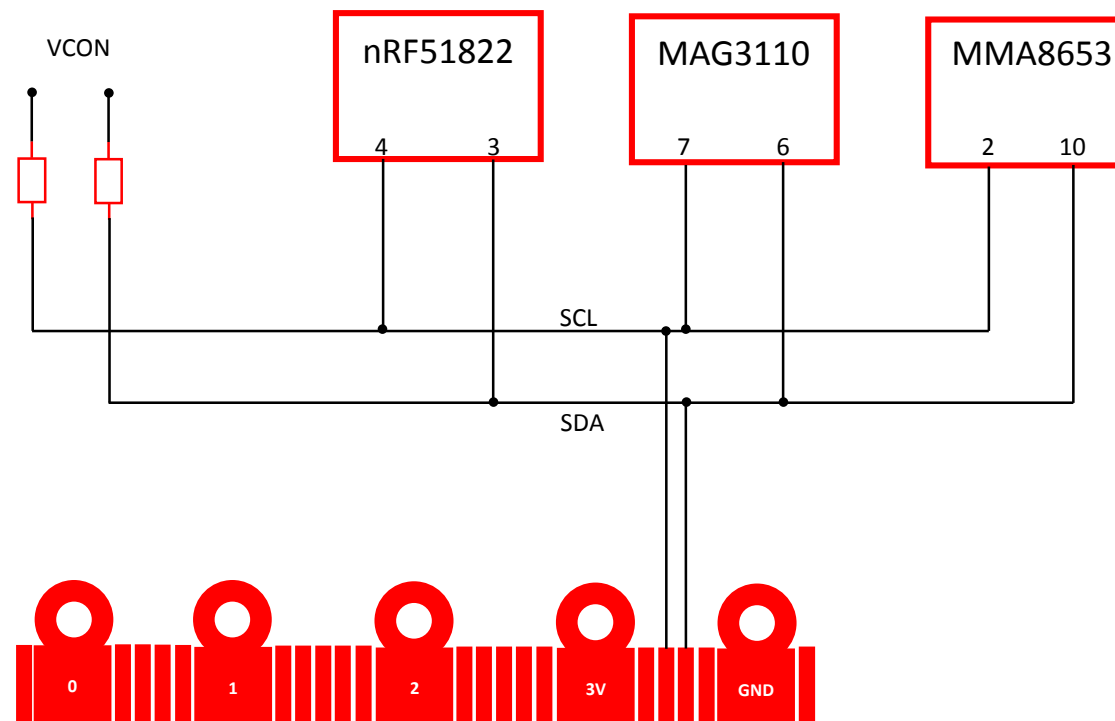
2

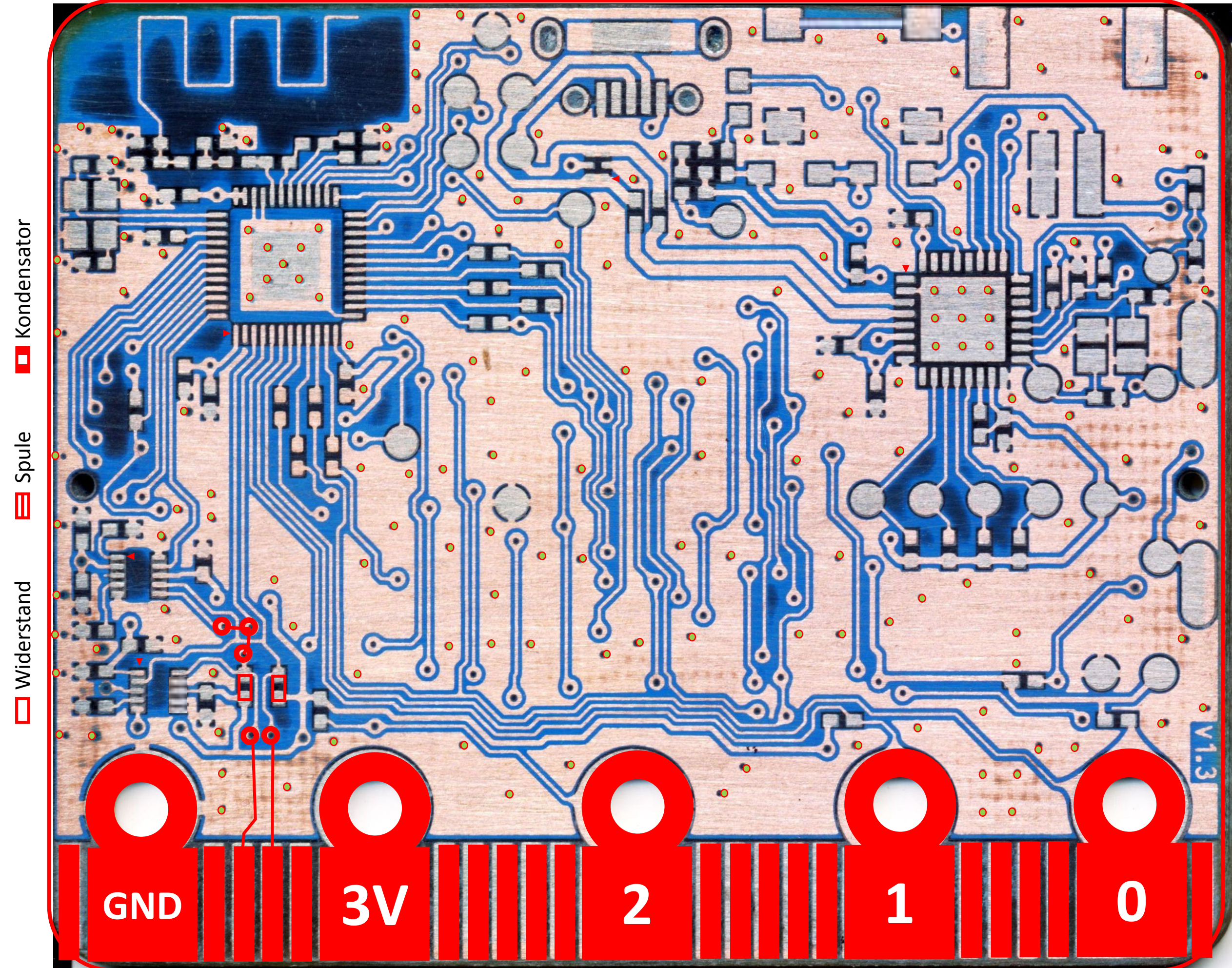
1

0

I2C

- 2 Leitungsbus (SCL, SDA)
- Pull-up = 4,7K
- Devices auf dem Bus:
 - nRF51822
 - MAG3110 = I2C Adr = 0x0E (7-Bit)
 - MMA8653 = I2C Adr = 0x1D (7-Bit)





Widerstand

Spule

Kondensator

GND

3V

2

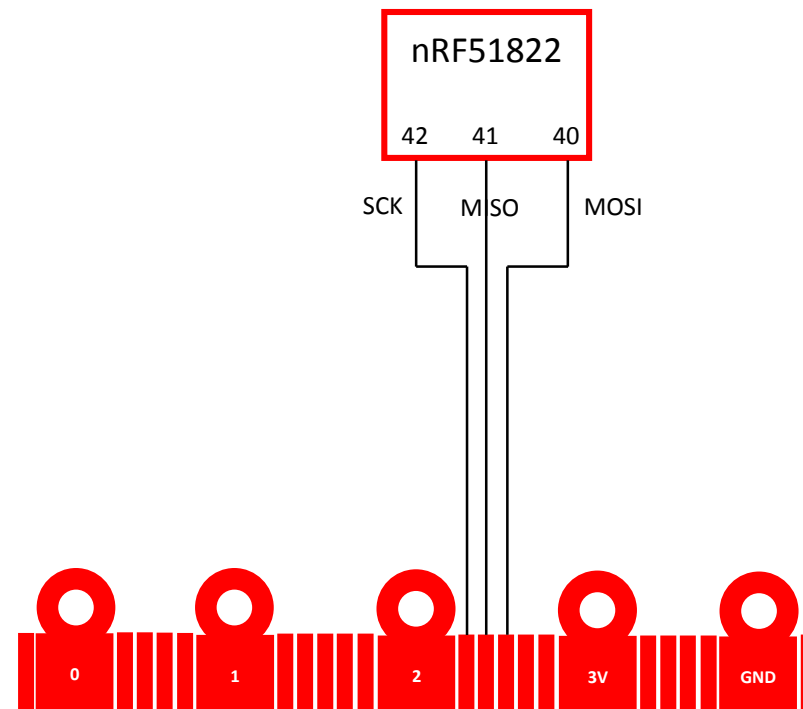
1

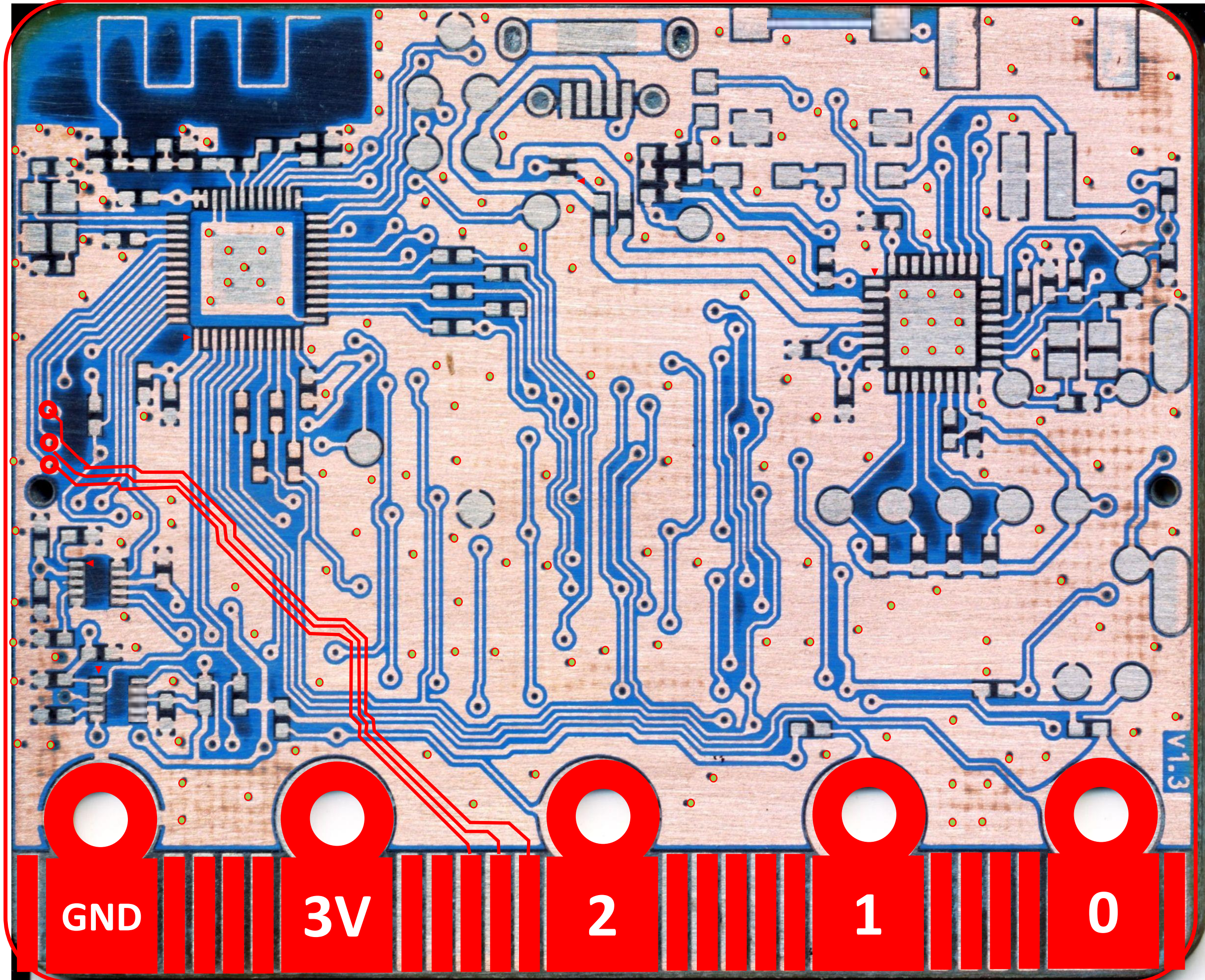
0

V1.3

SPI

- 3 Leitungsbus (SCK, MISO, MOSI)





GND

3V

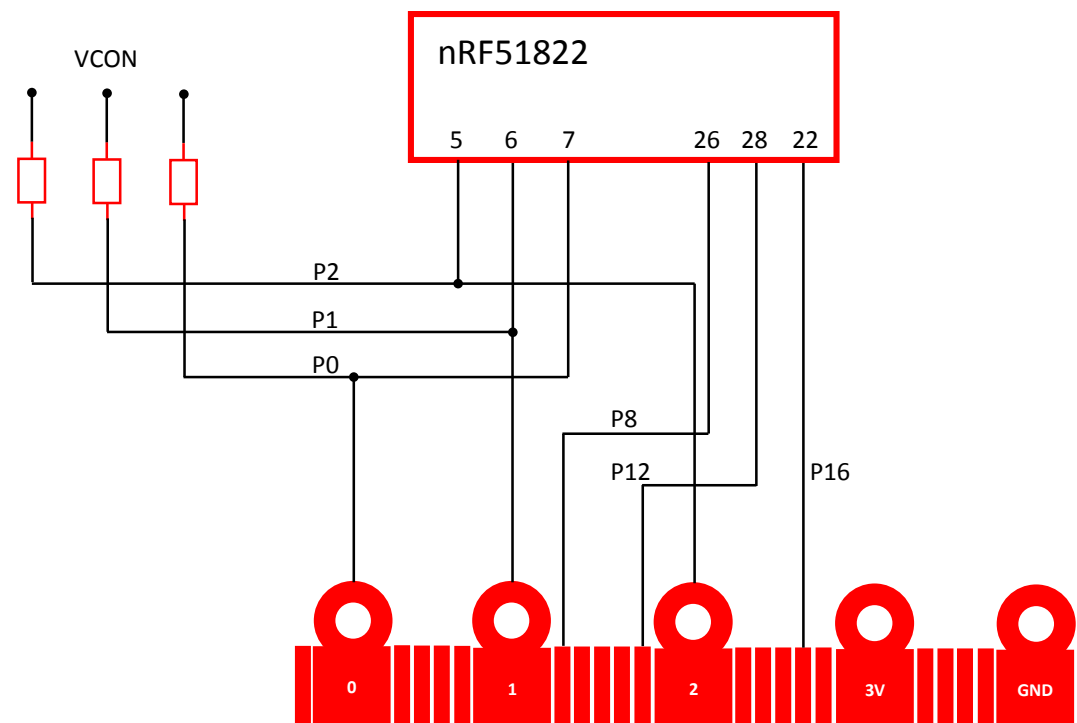
2

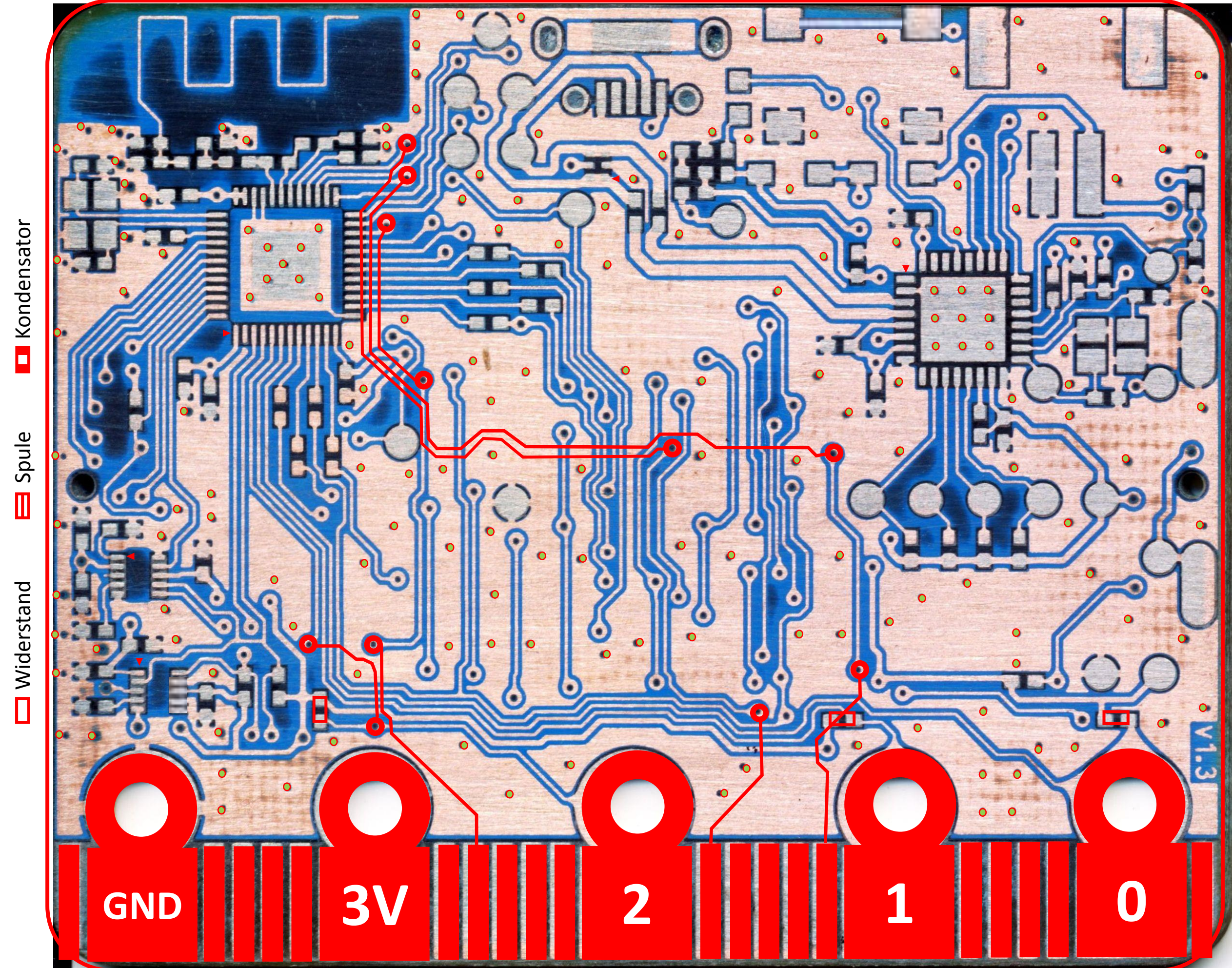
1

0

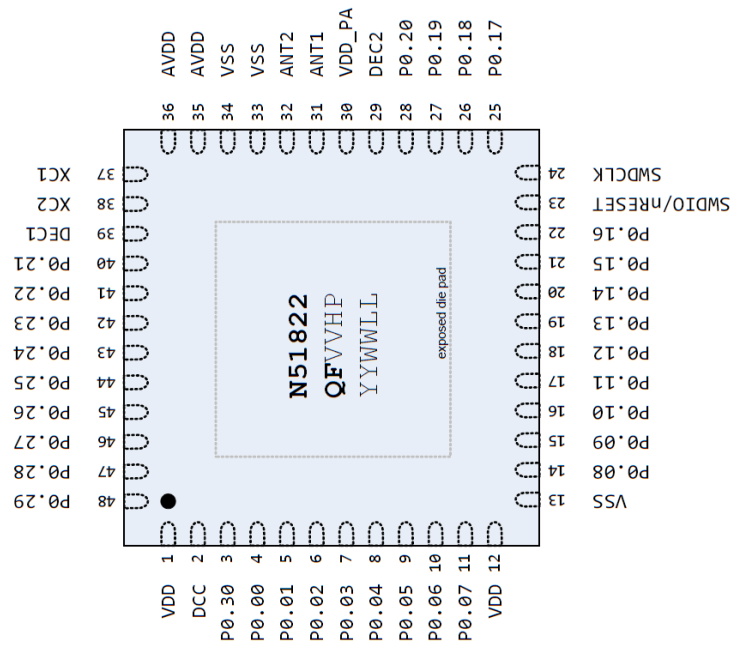
Ein- / Ausgänge

- 3 digitale / analoge Ein- bzw. Ausgänge (P0, P1, P2)
- 3 digitale Ein- bzw. Ausgänge (P8, P12, P16)

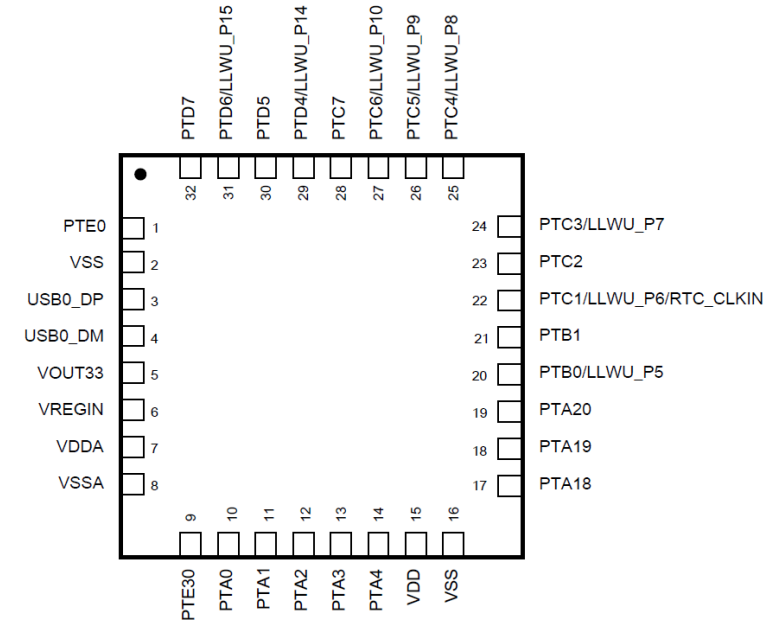




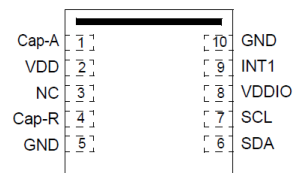
nRF51822



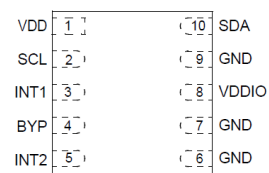
KL26Z



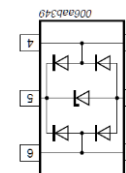
MAG3110



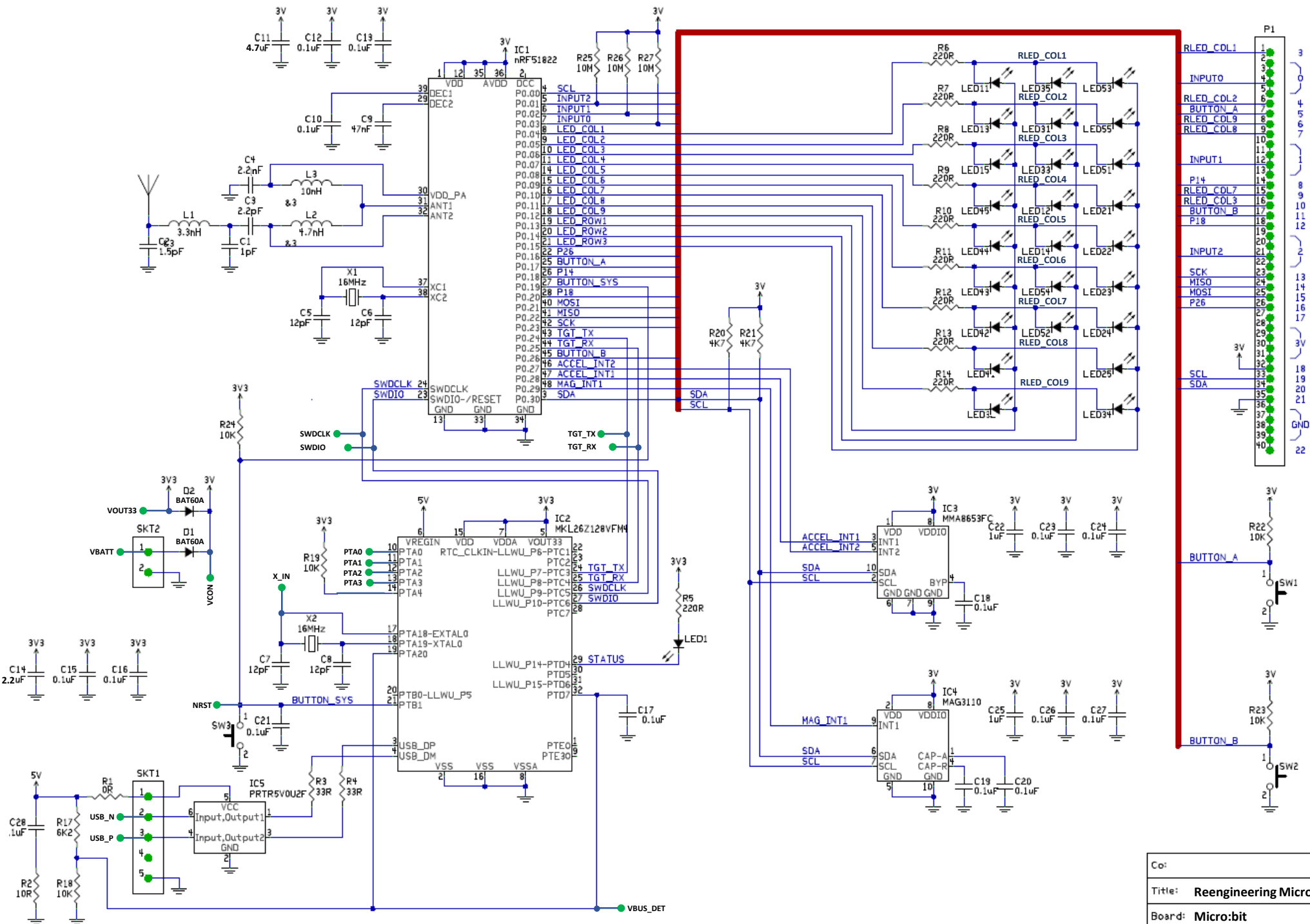
MMA8653



PRTR5V0U2F



Schematics



Co:	
Title:	Reengineering Micro:bit schematics
Board:	Micro:bit
Author:	Wolfgang
Date:	04.11.2016
Revision:	0.95
Size:	B
Sheet	1 of 1